

/ 10

MENU

SEARCH

INDEX

1/1



JAPANESE PATENT OFFICE

PATENT ABSTRACTS OF JAPAN

(11)Publication number: 06046088

(43)Date of publication of application: 18.02.1994

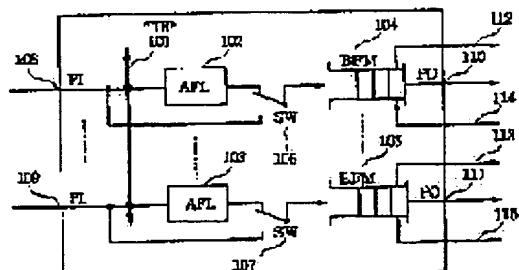
(51)Int.Cl.

H04L 12/56
H04Q 3/52(21)Application number: 04197972
(22)Date of filing: 24.07.1992(71)Applicant: NEC CORP
(72)Inventor: NAGANO HIROSHI(54) PACKET SWITCH

(57)Abstract:

PURPOSE: To easily realize the extension of a buffer memory arranged for each output port in the packet switch.

CONSTITUTION: A TDB 101 applies time division multiplex processing to a packet inputted from PI108-109. AFL 102-103 check address information of a packet on the TDB 101 and passes only the packet having a relevant address. SW 106-107 select a path for the packet from the AFL 102-103 or a path for a packet of the P1108-109 and the packet is stored in BFM 104-105. Overflow flags 112-113 of the BFM 104-105 are used to inform the overflow to devices at the outside of the switches and output stop command signals 114-115 from the devices at the outside of the switches stop packet output from the BFM 104-105.



LEGAL STATUS